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Substitute for form 1449/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>		Application Number	Not Yet Assigned
		Filing Date	11/24/2003
		First Named Inventor	Chuan-Ping Hou
		Art Unit	Not Yet Assigned
		Examiner Name	Not Yet Assigned
		Attorney Docket Number	N1085-00192
Sheet	1	of	2

**U. S. PATENT DOCUMENTS**

## **FOREIGN PATENT DOCUMENTS**

Examiner Signature	Karen Markee	Date Considered	03-09-05
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<b>NON PATENT LITERATURE DOCUMENTS</b>					
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
DHL	B	FU-LIANG YANG ET AL., "25 nm CMOS Omega FETs", 2002 IEEE, 4 pages			
DHL	C	S. MONFRAY ET AL., "50 nm - Gate All Around (GAA) - Silicon On Nothing (SON) - Devices: A Simple Way to Co-integration of GAA Transistors within bulk MOSFET process", 2002 IEEE, 2002 Symposium on VLSI Technology Digest of Technical Papers, 2 pages			
nm	D	J.P. COLINGE ET AL., "SOI Devices for Sub-0.1 um Gate Lengths", 2002 IEEE, 2002 Proc. 23rd International Conference on Microelectronics (Miel 2002), Vol 1, 2 pages			
nm	E	FU-LIANG YANG ET AL., "35 nm CMOS FinFETs", 2002 IEEE, 2002 Symposium on VLSI Technology Digest of Technical Papers, 2 pages			

Examiner Signature	<i>Karen</i>	Date Considered	02-07-05
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